



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Hiroyuki NANSEI et al.**

Group Art Unit: **2822**

Application Number: **10/643,967**

Examiner: **Toniae M. Thomas**

Filed: **August 20, 2003**

Confirmation Number: **4992**

For: **SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR  
MANUFACTURING SEMICONDUCTOR DEVICE**

Attorney Docket Number: **030993**

Customer Number: **38834**

**RESPONSE UNDER 37 C.F.R. § 1.111**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

August 22, 2005

Sir:

This paper is filed in response to the Office Action dated April 20, 2005.

Remarks begin on page 2 of this paper.